Mohan Lal Sukhadia University
Udaipur

B. Tech. Program
(Effective from session 2021-2022)

Electronics & Communication Engineering

Semesters III - VIII

Course structure

Teaching & Examination Scheme
Semester III

SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	BSC	BT3EC2-01	Advanced Engineering Mathematics-I	3	0	0	3	30	120	**150**	**3**
2	HSMC	BT3EC1-02/ BT3EC1-03	Technical Communication/Managerial Economics and Financial Accounting	2	0	0	2	20	80	**100**	**2**
3	PCC	BT3EC4-04	Digital System Design	3	0	0	3	30	120	**150**	**3**
4		BT3EC4-05	Signal & Systems	3	0	0	3	30	120	**150**	**3**
5		BT3EC4-06	Network Theory	3	1	0	3	40	160	**200**	**4**
6		BT3EC4-07	Electronics Devices	3	1	0	3	40	160	**200**	**4**
			Sub Total	17	2	0		190	760	**950**	**19**
PRACTICAL & SESSIONAL											
8	PCC	BT3EC4-21	Electronics Devices Lab	0	0	2		30	20	**50**	**1**
9		BT3EC4-22	Digital System Design Lab	0	0	2		30	20	**50**	**1**
10		BT3EC4-23	Signal Processing Lab	0	0	2		30	20	**50**	**1**
11	ESC	BT3EC3-24	Computer Programming Lab-I	0	0	2		30	20	**50**	**1**
13	PSIT	BT3EC7-30	Industrial Training	0	0	1				**50**	**1**
14	SODECA	BT3EC8-00	Social Outreach, Discipline & Extra Curricular Activities							**25**	**0.5**
			Sub- Total	0	0	9		120	80	**275**	**5.5**
		TOTAL OF III SEMESTER		17	2	9		310	840	**1225**	**24.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Teaching & Examination Scheme
Semester IV

THEORY											
SN	**Category**	**Course**		**Contact hrs/week**			**Marks**				**Cr**
		Code	**Title**	**L**	**T**	**P**	**Exm Hrs**	**IA**	**ETE**	**Total**	
1	BSC	BT4EC2-01	Advanced Engineering Mathematics-II	3	0	0	3	30	120	**150**	**3**
2	HSMC	BT4EC1-03/ BT4EC1-02	Managerial Economics and Financial Accounting/ Technical Communication	2	0	0	2	20	80	**100**	**2**
3	PCC	BT4EC4-04	Analog Circuits	3	0	0	3	30	120	**150**	**3**
4		BT4EC4-05	Microcontrollers	3	0	0	3	30	120	**150**	**3**
5	ESC	BT4EC3-06	Electronics Measurement & Instrumentation	3	0	0	3	30	120	**150**	**3**
6	PCC	BT4EC4-07	Analog and Digital Communication	3	0	0	3	30	120	**150**	**3**
			Sub Total	17	0	0		170	680	**850**	**17**
PRACTICAL & SESSIONAL											
8	PCC	BT4EC4-21	Analog and Digital Communication Lab	0	0	3		45	30	**75**	**1.5**
9		BT4EC4-22	Analog Circuits Lab	0	0	3		45	30	**75**	**1.5**
10		BT4EC4-23	Microcontrollers Lab	0	0	3		45	30	**75**	**1.5**
11		BT4EC4-24	Electronics Measurement & Instrumentation Lab	0	0	3		45	30	**75**	**1.5**
12	SODE CA	BT4EC18-00	Social Outreach, Discipline & Extra Curricular Activities							**25**	**0.5**
			Sub- Total	0	0	12		180	120	**325**	**6.5**
			TOTAL OF IV SEMEESTER	17	0	12		350	800	**1175**	**23.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Teaching & Examination Scheme

Semester V

THEORY											
SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
1	ESC	BT5EC 3-01	Computer Architecture	2	0	0	2	20	80	100	2
2	PCC/ PEC	BT5EC 4-02	Electromagnetics Waves	3	0	0	3	30	120	150	3
3		BT5EC 4-03	Control system	3	0	0	3	30	120	150	3
4		BT5EC 4-04	Digital Signal Processing	3	0	0	3	30	120	150	3
5		BT5EC 4-05	Microwave Theory & Techniques	3	0	0	3	30	120	150	3
6		Professional Elective I (any one)		2	0	0	2	20	80	100	2
		BT5EC 5-11	Bio-Medical Electronics								
		BT5EC 5-12	Embedded Systems								
		BT5EC 5-13	Probability Theory & Stochastic Process								
		BT5EC 5-14	Satellite Communication								
		Sub Total		16	0	0		160	640	800	16
PRACTICAL & SESSIONAL											
7	PCC	BT5EC 4-21	RF Simulation Lab	0	0	3	2	45	30	75	1.5
8		BT5EC 4-22	Digital Signal Processing Lab	0	0	3	2	45	30	75	1.5
9		BT5EC 4-23	Microwave Lab	0	0	2	2	30	20	50	1
10	PSIT	BT5EC 7-30	Industrial Training	0	0	1		75	50	125	2.5
11	SODECA	BT5EC 8-00	Social Outreach, Discipline & Extra Curricular Activities	0	0	0			25	25	0.5
		Sub- Total		0	0	9		195	155	350	7
		TOTAL OF V SEMESTER		16	0	9		355	795	1150	23

L: Lecture, T: Tutorial, P: Practical, Cr: Credits

ETE: End Term Exam, IA: Internal Assessment

Teaching & Examination Scheme

Semester VI

THEORY											
SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
1	ESC	BT6EC 3-01	Power Electronics	2	0	0	2	20	80	100	2
2	PCC/ PEC	BT6EC 4-02	Computer Network	3	0	0	3	30	120	150	3
3		BT6EC 4-03	Fiber Optics Communications	3	0	0	3	30	120	150	3
4		BT6EC 4-04	Antennas and Propagation	3	0	0	3	30	120	150	3
5		BT6EC 4-05	Information theory and coding	3	0	0	3	30	120	150	3
6		Professional Elective II (any one)		3	0	0	3	30	120	150	3
		BT6EC 5-11	Introduction to MEMS								
		BT6EC 5-12	Nano Electronics								
		BT6EC 5-13	Neural Network And Fuzzy Logic Control								
		BT6EC 5-14	High Speed Electronics								
		Sub Total		17	0	0		170	680	850	17
PRACTICAL & SESSIONAL											
7	PCC	BT6EC 4-21	Computer Network Lab	0	0	4	2	60	40	100	2
8		BT6EC 4-22	Antenna and wave propagation Lab	0	0	2	2	30	20	50	1
9		BT6EC 4-23	Electronics Design Lab	0	0	4	2	60	40	100	2
10		BT6EC 4-24	Power Electronics Lab	0	0	2	2	30	20	50	1
11	SODECA	BT6EC 8-00	Social Outreach, Discipline & Extra Curricular Activities	0	0	0			25	25	0.5
		Sub- Total		0	0	12		180	145	325	6.5
		TOTAL OF VI SEMESTER		17	0	12		350	825	1175	23.5

L: Lecture, T: Tutorial, P: Practical, Cr: Credits
ETE: End Term Exam, IA: Internal Assessment

Teaching & Examination Scheme
VII Semester

SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	PEC		Program Elective	3	0	0	3	30	120	**150**	**3**
		BT7EC5-11	VLSI Design								
		BT7EC5-12	Mixed Signal Design								
		BT7EC5-13	CMOS design								
2	OE		Open Elective-I	3	0	0	3	30	120	**150**	**3**
			Sub Total	**6**	**0**	**0**		**60**	**240**	**300**	**6**
PRACTICAL & SESSIONAL											
3	PCC	BT7EC4-21	VLSI Design Lab	0	0	4	2	60	40	**100**	**2**
4		BT7EC4-22	Advance communication lab (MATLAB Simulation)	0	0	2	2	30	20	**50**	**1**
5		BT7EC4-23	Optical Communication Lab	0	0	2	2	30	20	**50**	**1**
6	PSIT	BT7EC7-30	Industrial Training	1	0	0		75	50	**125**	**2.5**
7		BT7EC7-40	Seminar	2	0	0		60	40	**100**	**2**
8	SODECA	BT7EC8-00	Social Outreach, Discipline & Extra Curricular Activities					0	25	**25**	**0.5**
			Sub Total	**3**	**0**	**8**		**255**	**195**	**450**	**9**
			TOTAL of VII SEMESTER	**9**	**0**	**8**		**315**	**435**	**750**	**15**

L: Lecture, T: Tutorial, P: Practical, Cr: Credits
ETE: End Term Exam, IA: Internal Assessment

Teaching & Examination Scheme
VIII Semester

SN	Category	Course Code	Course Title	Contact hrs/week			Marks				Cr
				L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	PEC		Program Elective	3	0	0	3	30	120	**150**	**3**
		BT8EC5-11	Artificial Intelligence And Expert Systems								
		BT8EC5-12	Digital Image and Video Processing								
		BT8EC5-13	Adaptive Signal Processing								
2	OE		Open Elective-II	3	0	0	3	30	120	**150**	**3**
			Sub Total	**6**	**0**	**0**		**60**	**240**	**300**	**6**
PRACTICAL & SESSIONAL											
3	PCC	BT8EC4-21	Internet of Things (IOT) Lab	0	0	2	2	30	20	**50**	**1**
4		BT8EC4-22	Skill Development Lab	0	0	2	2	30	20	**50**	**1**
5	PSIT	BT8EC7-50	Project	3	0	0		210	140	**350**	**7**
6	SODECA	BT8EC8-00	Social Outreach, Discipline & Extra Curricular Activities						25	**25**	**0.5**
			Sub Total	**3**	**0**	**4**		**270**	**205**	**475**	**9.5**
			TOTAL of VIII SEMESTER	**9**	**0**	**4**		**330**	**445**	**775**	**15.5**

L: Lecture, T: Tutorial, P: Practical, Cr: Credits
ETE: End Term Exam, IA: Internal Assessment

List of Open Electives				
Subject Code	**Title**		**Subject Code**	**Title**
Open Elective - I			**Open Elective - II**	
BT7AG6-60.1	Human Engineering and Safety		BT8AG6-60.1	Energy Management
BT7AG6-60.2	Environmental Engineering and Disaster Management		BT8AG6-60.2	Waste and By-product Utilization
BT7AN6-60.1	Aircraft Avionic System		BT8AN6-60.1	Finite Element Methods
BT7AN6-60.2	Non-Destructive Testing		BT8AN6-60.2	Factor of Human Interactions
BT7CH6-60.1	Optimization Techniques		BT8CH6-60.1	Refinery Engineering Design
BT7CH6-60.2	Sustainable Engineering		BT8CH6-60.2	Fertilizer Technology
BT7CR6-60.1	Introduction to Ceramic Science & Technology		BT8CR6-60.1	Electrical and Electronic Ceramics
BT7CR6-60.2	Plant, Equipment and Furnace Design		BT8CR6-60.2	Biomaterials
BT7CE6-60.1	Environmental Impact Analysis		BT8CE6-60.1	Composite Materials
BT7CE6-60.2	Disaster Management		BT8CE6-60.2	Fire and Safety Engineering
BT7EE6-60.1	Electrical Machines and Drives		BT8EE6-60.1	Energy Audit and Demand side Management
BT7EE6-60.2	Power Generation Sources.		BT8EE6-60.2	Soft Computing
BT7EC6-60.1	Principle of Electronic communication		BT8EC6-60.1	Industrial and Biomedical applications of RF Energy
BT7EC6-60.2	Micro and Smart System Technology		BT8EC6-60.2	Robotics and control
BT7ME6-60.1	Finite Element Analysis		BT8ME6-60.1	Operations Research
BT7ME6-60.2	Quality Management		BT8ME6-60.2	Simulation Modeling and Analysis
BT7MI6-60.1	Rock Engineering		BT8MI6-60.1	Experimental Stress Analysis
BT7MI6-60.2	Mineral Processing		BT8MI6-60.2	Maintenance Management
BT7PE6-60.1	Pipeline Engineering		BT8PE6-60.1	Unconventional Hydrocarbon Resources
BT7PE6-60.2	Water Pollution control Engineering		BT8PE6-60.2	Energy Management & Policy
BT7TT6-60.1	Technical Textiles		BT8TT6-60.1	Material and Human Resource Management
BT7TT6-60.2	Garment Manufacturing Technology		BT8TT6-60.2	Disaster Management

Mohan Lal Sukhadia University
Udaipur

B. Tech. Program
(Effective from session 2021-2022)

Electronics & Communication Engineering

Semesters III

Syllabus

BT3EC2-01: Advance Engineering Mathematics-I

3 Credits **Max. Marks: 150 (IA:30, ETE:120)**
3L:0T:0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Numerical Methods – 1:** Finite differences, Relation between operators, Interpolation using Newton's forward and backward difference formulae. Gauss's forward and backward interpolation formulae. Stirling's Formulae. Interpolation with unequal intervals: Newton's divided difference and Lagrange's formulae. Numerical Differentiation, Numerical integration: Trapezoidal rule and Simpson's 1/3rd and 3/8 rules.	**10**
2	**Numerical Methods – 2:** Numerical solution of ordinary differential equations: Taylor's series, Euler and modified Euler's methods. Runge- Kutta method of fourth order for solving first and second order equations. Milne's and Adam's predicator-corrector methods. Solution of polynomial and transcendental equations-Bisection method, Newton-Raphson method and Regula-Falsi method.	**8**
3	**Laplace Transform:** Definition and existence of Laplace transform, Properties of Laplace Transform and formulae, Unit Step function, Dirac Delta function, Heaviside function, Laplace transform of periodic functions. Finding inverse Laplace transform by different methods, convolution theorem. Evaluation of integrals by Laplace transform, solving ODEs by Laplace transforms method.	**10**
4	**Fourier Transform:** Fourier Complex, Sine and Cosine transform, properties and formulae, inverse Fourier transforms, Convolution theorem, application of Fourier transforms to partial ordinary differential equation (One dimensional heat and wave equations only).	**7**
5	**Z-Transform:** Definition, properties and formulae, Convolution theorem, inverse Z-transform, application of Z-transform to difference equation.	**5**
	Total	**40**

BT3EC1-02/BT4EC1-02: Technical Communication

2 Credit **Max. Marks: 100 (IA:20, ETE:80)**

2L:0T:0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	4
2	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Readingand comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note- making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
3	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
4	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	Total	26

BT3EC1-03/BT4EC1-03: Managerial Economics And Financial Accounting

2 Credit **Max. Marks: 100 (IA:20, ETE:80)**

2L:0T:0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	Basic economic concepts- Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	**4**
2	Demand and Supply analysis- Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	**5**
3	Production and Cost analysis- Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	**5**
4	Market structure and pricing theory- Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	**4**
5	Financial statement analysis- Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	**8**
	Total	**26**

BT3EC4-04: Digital System Design

2 Credits **Max. Marks: 150 (IA:30, ETE:120)**

3L:0T:0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Logic Simplification and Combinational Logic Design: Review of Boolean Algebra and De Morgan's Theorem, SOP & POS forms, Canonical forms, Karnaugh maps up to 6 variables, Binary codes, Code Conversion.	**7**
2	MSI devices like Comparators, Multiplexers, Encoder, Decoder, Driver & Multiplexed Display, Half and Full Adders, Subtractors, Serial and Parallel Adders, BCD Adder, Barrel shifter and ALU	**8**
3	Sequential Logic Design: Building blocks like S-R, JK and Master- Slave JK FF, Edge triggered FF, Ripple and Synchronous counters, Shift registers, Finite state machines, Design of Synchronous FSM, Algorithmic State Machines charts. Designing synchronous circuits like Pulse train generator, Pseudo Random Binary Sequence generator, Clock generation.	**9**
4	Logic Families and Semiconductor Memories: TTL NAND gate, Specifications, Noise margin, Propagation delay, fan-in, fan-out, Tristate TTL, ECL, CMOS families and their interfacing, memory elements, Concept of Programmable logic devices like FPGA. Logic implementation using programmable devices.	**8**
5	VLSI Design flow: Design entry: Schematic, FSM & HDL, different modeling styles in VHDL, Data types and objects, Dataflow, Behavioral and Structural Modeling, Synthesis and Simulation VHDL constructs and codes for combinational and sequential circuits.	**8**
	Total	**40**

BT3EC4-05: Signals & Systems

3 Credits **Max. Marks: 150 (IA:30, ETE:120)**

3L:0T:0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Energy and power signals, continuous and discrete time signals, continuous and discrete amplitude signals. System properties: linearity: additivity and homogeneity, shift-invariance, causality, stability, realizability.	**6**
2	Linear shift-invariant (LSI) systems, impulse response and step response, convolution, input output behavior with aperiodic convergent inputs. Characterization of causality and stability of linear shift-invariant systems. System representation through differential equations and difference equations	**7**
3	Periodic and semi-periodic inputs to an LSI system, the notion of a frequency response and its relation to the impulse response, Fourier series representation, the Fourier Transform, convolution/multiplication and their effect in the frequency domain, magnitude and phase response, Fourier domain duality. The Discrete-Time Fourier Transform (DTFT) and the Discrete Fourier Transform (DFT). Parseval's Theorem. The idea of signal space and orthogonal bases	**8**
4	The Laplace Transform, notion of eigen functions of LSI systems, a basis of eigen functions, region of convergence, poles and zeros of system, Laplace domain analysis, solution to differential equations and system behavior.	**6**
5	The z-Transform for discrete time signals and systems- eigen functions, region of convergence, z-domain analysis.	**5**
6	State-space analysis and multi-input, multi-output representation. The state-transition matrix and its role. The Sampling Theorem and its implications- Spectra of sampled signals. Reconstruction: ideal interpolator, zero-order hold, first- order hold, and so on. Aliasing and its effects. Relation between continuous and discrete time systems.	**8**
	Total	**40**

BT3EC4-06: Network Theory

3 Credits **Max. Marks: 200 (IA:40, ETE:160)**

3L:1T:0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Node and Mesh Analysis, matrix approach of network containing voltage and current sources, and reactances, source transformation and duality.	**7**
2	Network theorems: Superposition, reciprocity, Thevenin's, Norton's, Maximum power Transfer, compensation and Tallegen's theorem as applied to AC. circuits.	**7**
3	Trigonometric and exponential Fourier series: Discrete spectra and symmetry of waveform, steady state response of a network to non- sinusoidal periodic inputs, power factor, effective values, Fourier transform and continuous spectra, three phase unbalanced circuit and power calculation.	**8**
4	Laplace transforms and properties: Partial fractions, singularity functions, waveform synthesis, analysis of RC, RL, and RLC networks with and without initial conditions with Laplace transforms evaluation of initial conditions..	**8**
5	Transient behavior, concept of complex frequency, Driving points and transfer functions poles and zeros of immittance function, their properties, sinusoidal response from pole-zero locations, convolution theorem and Two four port network and interconnections, Behaviors of series and parallel resonant circuits, Introduction to band pass, low pass, high pass and band reject filters.	**10**
	Total	**40**

BT3EC4-07: Electronic Devices

4 Credits **Max. Marks: 200 (IA:40, ETE:160)**

3L:1T:0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction to Semiconductor Physics: Introduction, Energy band gap structures of semiconductors, Classifications of semiconductors, Degenerate and non-degenerate semiconductors, Direct and indirect band gap semiconductors, Electronic properties of Silicon, Germanium, Compound Semiconductor, Gallium Arsenide, Gallium phosphide & Silicon carbide, Variation of semiconductor conductivity, resistance and bandgap with temperature and doping. Thermistors, Sensitors.	**6**
2	Review of Quantum Mechanics, Electrons in periodic Lattices, E-k diagrams. Energy bands in intrinsic and extrinsic silicon; Carrier transport: diffusion current, drift current, mobility and resistivity; sheet resistance, design of resistors.	**6**
3	Generation and recombination of carriers; Poisson and continuity equation P-N junction characteristics, I-V characteristics, and small signal switching models; Avalanche breakdown, Zener diode, Schottky diode.	**8**
4	Bipolar Junction Transistor, I-V characteristics, Ebers-Moll Model, MOS capacitor, C-V characteristics, MOSFET, I-V characteristics, and small signal models of MOS transistor, LED, photodiode and solar cell.	**11**
5	Integrated circuit fabrication process: oxidation, diffusion, ion implantation, Photolithography, etching, chemical vapor deposition, sputtering, twin-tub CMOS process.	**9**
	Total	**40**

BT3EC4-21: Electronics Devices Lab

1 Credit **Max. Marks: 50 (IA:30, ETE:20) 0L:0T:2P**

List of Experiments

Sr. No.	Name of Experiment
1.	Study the following devices: (a) Analog& digital multimeters (b) Function/ Signal generators (c) Regulated d. c. power supplies (constant voltage and constant current operations) (d) Study of analog and digital CRO, measurement of time period, amplitude, frequency & phase angle using Lissajous figures.
2.	Plot V-I characteristic of P-N junction diode & calculate cut-in voltage, reverse Saturation current and static & dynamic resistances.
3.	Plot the output waveform of half wave rectifier and effect of filters on waveform. Also calculate its ripple factor.
4.	Study bridge rectifier and measure the effect of filter network on D.C. voltage output & ripple factor.
5.	Plot and verify output waveforms of different clipper and clamper.
6.	Plot V-I characteristic of Zener diode
7.	Study of Zener diode as voltage regulator. Observe the effect of load changes and determine load limits of the voltage regulator
8.	Plot input-output characteristics of BJT in CB, CC and CE configurations. Find their h-parameters.
9.	Study of different biasing circuits of BJT amplifier and calculate its Q- point.
10.	Plot frequency response of two stage RC coupled amplifier & calculate its bandwidth .

11.	Plot input-output characteristics of field effect transistor and measure I_{dss} and V_p.
12.	Plot frequency response curve for FET amplifier and calculate its gain bandwidth product.

BT3EC4-22: Digital System Design Lab

1 Credit **Max. Marks: 50 (IA:30, ETE:20) 0L:0T:2P**

List of Experiments

S.No.	**Name of Experiment**
Part A: Combinational Circuits	
1.	To verify the truth tables of logic gates: AND, OR, NOR, NAND, NOR, Ex-OR and Ex-NOR
2.	To verify the truth table of OR, AND, NOR, Ex-OR, Ex-NOR logic gates realized using NAND & NOR gates.
3.	To realize an SOP and POS expression.
4.	To realize Half adder/ Subtractor& Full Adder/ Subtractor using NAND & NOR gates and to verify their truth tables
5.	To realize a 4-bit ripple adder/ Subtractor using basic Half adder/ Subtractor & basic Full Adder/ Subtractor.
6.	To design 4-to-1 multiplexer using basic gates and verify the truth table. Also verify the truth table of 8-to-1 multiplexer using IC
7.	To design 1-to-4 demultiplexer using basic gates and verify the truth table. Also to construct 1-to-8 demultiplexer using blocks of 1-to-4 demultiplexer
8.	To design 2x4 decoder using basic gates and verify the truth table. Also verify the truth table of 3x8 decoder using IC
9.	Design & Realize a combinational circuit that will accept a 2421 BCD code and drive a TIL -312 seven-segment display
Part B: Sequential Circuits	
10.	Using basic logic gates, realize the R-S, J-K and D-flip flops with and without clock signal and verify their truth table.
11.	Construct a divide by 2, 4 & 8 asynchronous counter. Construct a 4-bit binary counter and ring counter for a particular output pattern using D flip flop.
12.	Design and construct unidirectional shift register and verify the
13.	Design and construct BCD ripple counter and verify the function.

14.	Design and construct a 4 Bit Ring counter and verify the function
15.	Perform input/output operations on parallel in/Parallel out and Serial in/Serial out registers using clock. Also exercise loading only one of multiple values into the register using multiplexer.

Note: Minimum 6 experiments to be conducted from **Part-A**& 4 experiments to be conducted from **Part-B.**

BT3EC4-23: Signal Processing Lab

1 Credit **Max. Marks: 50 (IA:30, ETE:20) 0L:0T:2P**

List of Experiments

Sr. No.	**Name of Experiment (Simulate using MATLAB environment)**
1.	Generation of continuous and discrete elementary signals (periodic and non periodic) using mathematical expression.
2.	Generation of Continuous and Discrete Unit Step Signal.
3.	Generation of Exponential and Ramp signals in Continuous & Discrete domain.
4.	Continuous and discrete time Convolution (using basic definition).
5.	Adding and subtracting two given signals. (Continuous as well as Discrete signals)
6.	To generate uniform random numbers between (0, 1).
7.	To generate a random binary wave.
8.	To generate and verify random sequences with arbitrary distributions, means and variances for following: (a) Rayleigh distribution (b) Normal distributions: N(0,1). (c) Gaussian distributions: N (m, x)
9.	To plot the probability density functions. Find mean and variance for the above distributions

BT3EC3-24: Computer Programming Lab-I

1 Credit **Max. Marks: 50 (IA:30, ETE:20) 0L:0T:2P**

1.	Write a simple C program on a 32 bit compiler to understand the concept of array storage, size of a word. The program shall be written illustrating the concept of row major and column major storage. Find the address of element and verify it with the theoretical value. Program may be written for arrays upto 4-dimensions.
2.	Simulate a stack, queue, circular queue and dequeue using a one dimensional array as storage element. The program should implement the basic addition, deletion and traversal operations.
3.	Represent a 2-variable polynomial using array. Use this representation to implement addition of polynomials.
4.	Represent a sparse matrix using array. Implement addition and transposition operations using the representation.
5.	Implement singly, doubly and circularly connected linked lists illustrating operations like addition at different locations, deletion from specified locations and traversal.
6.	Repeat exercises 2, 3 & 4 with linked structures.
7.	Implementation of binary tree with operations like addition, deletion, traversal.
8.	Depth first and breadth first traversal of graphs represented using adjacency matrix and list.
9.	Implementation of binary search in arrays and on linked Binary Search Tree.
10.	Implementation of insertion, quick, heap, topological and bubble sorting algorithms.

Mohan Lal Sukhadia University
Udaipur

B. Tech. Program
(Effective from session 2021-2022)

Electronics & Communication Engineering

Semesters IV

Syllabus

BT4EC2-01: Advance Engineering Mathematics-II

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Complex Variable – Differentiation:** Differentiation, Cauchy-Riemann equations, analytic functions, harmonic functions, finding harmonic conjugate; elementary analytic functions (exponential, trigonometric, logarithm) and their properties; Conformal mappings, Mobius transformations and their properties.	7
3	**Complex Variable - Integration:** Contour integrals, Cauchy-Goursat theorem (without proof), Cauchy Integral formula (without proof), Liouville's theorem and Maximum-Modulus theorem (without proof); Taylor's series, zeros of analytic functions, singularities, Laurent's series; Residues, Cauchy Residue theorem (without proof).	8
4	**Applications of complex integration by residues:** Evaluation of definite integral involving sine and cosine. Evaluation of certain improper integrals.	4
5	**Special Functions:** Legendre's function, Rodrigues formula, generating function, Simple recurrence relations, orthogonal property. Bessel's functions of first and second kind, generating function, simple recurrence relations, orthogonal property.	10
6	**Linear Algebra:** Vector Spaces, subspaces, Linear independence, basis and dimension, Inner product spaces, Orthogonality, Gram Schmidt orthogonalization, characteristic polynomial, minimal polynomial, positive definite matrices and canonical forms, QR decomposition.	10
	Total	40

BT4EC1-03/BT3EC1-03: Managerial Economics And Financial Accounting

2 Credit **Max. Marks: 100 (IA:20, ETE:80)**

2L:0T:0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Basic economic concepts:** Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	3
3	**Demand and Supply analysis:** Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
4	**Production and Cost analysis:** Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
5	**Market structure and pricing theory:** Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
6	**Financial statement analysis:** Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	Total	26

BT4EC1-02/BT3EC1-02: Technical Communication

2 Credit **Max. Marks: 100 (IA:20, ETE:80)**
2L:0T:0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	3
3	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Reading and comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note- making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
4	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
5	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	Total	26

BT4EC4-04: Analog Circuits

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Diode Circuits, Amplifier models: Voltage amplifier, current amplifier, trans-conductance amplifier and trans-resistance amplifier. Biasing schemes for BJT and FET amplifiers, bias stability, various configurations (such as CE/CS, CB/CG, CC/CD) and their features, small signal analysis, low frequency transistor models, estimation of voltage gain, input resistance, output resistance etc., design procedure for particular specifications, low frequency analysis of multistage amplifiers.	8
3	High frequency transistor models, frequency response of single stage and multistage amplifiers, cascode amplifier. Various classes of operation (Class A, B, AB, C etc.), their power efficiency and linearity issues. Feedback topologies: Voltage series, current series, voltage shunt, current shunt, effect of feedback on gain, bandwidth etc., calculation with practical circuits, concept of stability, gain margin and phase margin.	8
4	Oscillators: Review of the basic concept, Barkhausen criterion, RC oscillators (phase shift, Wien bridge etc.), LC oscillators (Hartley, Colpitt, Clapp etc.), non-sinusoidal oscillators. Current mirror: Basic topology and its variants, V-I characteristics, output resistance and minimum sustainable voltage (VON), maximum usable load. Differential amplifier: Basic structure and principle of operation, calculation of differential gain, common mode gain, CMRR and ICMR. OP-AMP design: design of differential amplifier for a given specification, design of gain stages and output stages, compensation.	8
5	OP-AMP applications: review of inverting and non-inverting amplifiers, integrator and differentiator, summing amplifier, precision rectifier, Schmitt trigger and its applications. Active filters: Low pass, high pass, band pass and band stop, design guidelines.	8
6	Digital-to-analog converters (DAC): Weighted resistor, R-2R ladder, resistor string etc. Analog to digital converters (ADC): Single slope, dual slope, successive approximation, flash etc. Switched capacitor circuits: Basic concept, practical configurations, application in amplifier, integrator, ADC etc.	7
	Total	40

BT4EC4-05: Microcontrollers

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3**

Hours

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Overview of microcomputer systems and their building blocks, memory interfacing, concepts of interrupts and Direct Memory Access, instruction sets of microprocessors (with examples of 8085 and 8086);	10
3	Interfacing with peripherals - timer, serial I/O, parallel I/O, A/D and D/A converters; Arithmetic Coprocessors; System level interfacing design;	8
4	Concepts of virtual memory, Cache memory, Advanced coprocessor Architectures- 286, 486, Pentium; Microcontrollers: 8051 systems,	10
5	Introduction to RISC processors; ARM microcontrollers interface designs.	11
	Total	40

BT4EC3-06: Electronics Measurement & Instrumentation

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3**

Hours

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**THEORY OF ERRORS** - Accuracy & precision, Repeatability, Limits of errors, Systematic & random errors, Modeling of errors, Probable error & standard deviation, Gaussian error analysis, Combination of errors.	8
3	**ELECTRONIC INSTRUMENTS** - Electronic Voltmeter, Electronic Multimeters, Digital Voltmeter, and Component Measuring Instruments: Q meter, Vector Impedance meter, RF Power & Voltage Measurements, Introduction to shielding & grounding.	8

4	**OSCILLOSCOPES** – CRT Construction, Basic CRO circuits, CRO Probes, Techniques of Measurement of frequency, Phase Angle and Time Delay, Multibeam, multi trace, storage & sampling Oscilloscopes.	**7**
5	**SIGNAL GENERATION AND SIGNAL ANALYSIS** - Sine wave generators, Frequency synthesized signal generators, Sweep frequency generators. Signal Analysis - Measurement Technique, Wave Analyzers, and Frequency - selective wave analyser, Heterodyne wave analyser, Harmonic distortion analyser, and Spectrum analyser.	**8**
6	**TRANSDUCERS** - Classification, Selection Criteria, Characteristics, Construction, Working Principles and Application of following Transducers:- RTD, Thermocouples, Thermistors, LVDT, Strain Gauges, Bourdon Tubes, Seismic Accelerometers, Tachogenerators, Load Cell, Piezoelectric Transducers, Ultrasonic Flow Meters.	**8**
	Total	**40**

BT4EC4-07: Analog and Digital Communication

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	**1**
2	Review of signals and systems, Frequency domain representation of signals, Principles of Amplitude Modulation Systems- DSB, SSB and VSB modulations. Angle Modulation, Representation of FM and PM signals, Spectral characteristics of angle modulated signals.	**8**
3	Review of probability and random process. Gaussian and white noise characteristics, Noise in amplitude modulation systems, Noise in Frequency modulation systems. Pre-emphasis and Deemphasis, Threshold effect in angle modulation.	**7**
4	Pulse modulation. Sampling process. Pulse Amplitude and Pulse code modulation (PCM), Differential pulse code modulation. Delta modulation, Noise considerations in PCM, Time Division multiplexing, Digital Multiplexers.	**8**
5	Elements of Detection Theory, Optimum detection of signals in noise, Coherent communication with waveforms- Probability of Error evaluations. Baseband Pulse Transmission- Inter symbol Interference and Nyquist criterion. Pass band Digital Modulation schemes- Phase Shift Keying, Frequency Shift Keying, Quadrature Amplitude Modulation, Continuous Phase Modulation and Minimum Shift Keying.	**8**

6	Digital Modulation tradeoffs. Optimum demodulation of digital signals over band-limited channels- Maximum likelihood sequence detection (Viterbi receiver). Equalization Techniques. Synchronization and Carrier Recovery for Digital modulation.	**8**
	Total	**40**

BT4EC4-21: Analog and Digital Communication Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P

List of Experiments	
Sr. No.	**Name of Experiment**
1.	Observe the Amplitude modulated wave form & measure modulation index and demodulation of AM signal.
2.	Harmonic analysis of Amplitude Modulated wave form.
3.	Generation & Demodulation of DSB – SC signal.
4.	Modulate a sinusoidal signal with high frequency carrier to obtain FM signal and demodulation of the FM signal.
5.	Verification of Sampling Theorem.
6.	To study & observe the operation of a super heterodyne receiver.
7.	PAM, PWM & PPM: Modulation and demodulation.
8.	To observe the transmission of four signals over a single channel using TDM-PAM method.
9.	To study the PCM modulation & demodulation and study the effect of channel like attenuation, noise in between modulator & demodulator through the experimental setup.
10.	To study the 4 channel PCM multiplexing & de-multiplexing in telephony system.
11.	To study the Delta & Adaptive delta modulation & demodulation and also study the effect of channel like attenuation, noise in between modulator & demodulator through the experimental setup.
12.	To perform the experiment of generation and study the various data formatting schemes (Unipolar, Bipolar, Manchester, AMI etc.)
13.	To perform the experiment of generation and detection of ASK, FSK, BPSK, DBPSK signals with variable length data pattern.

BT4EC4-22: Analog Circuits Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P

List of Experiments	
Sr. No.	**Name of Experiment**
1.	Study and implementation of Voltage Series and Current Series Negative Feedback Amplifier.
2.	Study and implementation of Voltage Shunt and Current Shunt Negative Feedback Amplifier.
3.	Plot frequency response of BJT amplifier with and without feedback in the emitter circuit and calculate bandwidth, gain bandwidth product with and without negative feedback.
4.	Study and implementation of series and shunt voltage regulators and calculate line regulation and ripple factor.
5.	Plot and study the characteristics of small signal amplifier using FET.
6.	Study and implementation of push pull amplifier. Measure variation of output power & distortion with load and calculate the efficiency.
7.	Study and implementation of Wein bridge oscillator and observe the effect of variation in oscillator frequency.
8.	Study and implementation of transistor phase shift oscillator and observe the effect of variation in R & C on oscillator frequency and compare with theoretical value.
9.	Study and implementation of the following oscillators and observe the effect of variation of capacitance on oscillator frequency: (a) Hartley (b) Colpitts.
10.	Study and implementation of the Inverting And Non-Inverting Operational Amplifier.
11.	Study and implementation of Summing, Scaling And Averaging of Operational Amplifier
12.	Implementation of active filters using OPAMP.

BT4EC4-23: Microcontrollers Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P

List of Experiments	
Sr. No.	**Name of Experiment**
Following exercises has to be Performed on 8085	
1.	Write a program for Multiplication of two 8 bit numbers Division of two 8 bit numbers
2.	Write a program to arrange a set of data in Ascending and Descending order.
3.	Write a program to find Factorial of a given number.
4.	Write a program to generate a Software Delay. Using a Register Using a Register Pair
8085 Interfacing Programs	
5.	5.1 Write a program to Interface ADC with 8085.
	5.2 Write a program to interface Temperature measurement module with 8085.
6.	Write a program to interface Keyboard with 8085.
7.	Write a program to interface DC Motor and stepper motor with 8085.
Following exercises has to be Performed on 8051	
8.	Write a program to convert a given Hex number to Decimal.
9.	Write a program to find numbers of even numbers and odd numbersamong 10 Numbers.
10.	Write a program to find Largest and Smallest Numbers among 10 Numbers.
11.	11.1 To study how to generate delay with timer and loop.
	11.2 Write a program to generate a signal on output pin using timer.
8051 Interfacing Programs	
12	12.1 Write a program to interface Seven Segment Display with 8051.
	12.2 Write a program to interface LCD with 8051.
13	Write a program for Traffic light Control using 8051.
14	Write a program for Elevator Control using 8051.

BT4EC4-24: Electronics Measurement & Instrumentation Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P

List of Experiments	
Sr. No.	**Name of Experiment**
1.	Measure earth resistance using fall of potential method.
2.	Plot V-I characteristics & measure open circuit voltage & short circuit current of a solar panel.
3.	Measure unknown inductance capacitance resistance using following bridges (a) Anderson Bridge (b) Maxwell Bridge
4.	To measure unknown frequency & capacitance using Wein's bridge.
5.	Measurement of the distance with the help of ultrasonic transmitter & receiver.
6.	Measurement of displacement with the help of LVDT.
7.	Draw the characteristics of the following temperature transducers (a) RTD (Pt-100) (b) Thermistors.
8.	Draw the characteristics between temperature & voltage of a K type thermocouple
9.	Calibrate an ammeter using D.C. slide wire potentiometer
10.	Measurement of strain/force with the help of strain gauge load cell.
11.	Study the working of Q-meter and measure Q of coils.
12.	Calibrate a single-phase energy meter (Analog and Digital) by phantom loading at different power factor by: (i) Phase shifting transformer (ii) Auto transformer.

Mohan Lal Sukhadia University
Udaipur

B. Tech. Program
(Effective from session 2021-2022)

Electronics & Communication Engineering

Semesters V

Syllabus

BT5EC3-01: Computer Architecture

Credit: 2 **Max. Marks: 100(IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Basic Structure of Computers, Functional units, software, performance issues software, machine instructions and programs, Types of instructions, Instruction sets: Instruction formats, Assembly language, Stacks, Ques, Subroutines.	6
3	Processor organization, Information representation, number formats. Multiplication & division, ALU design, Floating Point arithmetic, IEEE 754 floating point formats	5
4	Control Design, Instruction sequencing, Interpretation, Hard wired control Design methods, and CPU control unit. Microprogrammed Control - Basic concepts, minimizing microinstruction size, multiplier control unit. Microprogrammed computers - CPU control unit	6
5	Memory organizations, device characteristics, RAM, ROM, Memory management, Concept of Cache & associative memories, Virtual memory.	5
6	System organization, Input - Output systems, Interrupt, DMA, Standard I/O interfaces Concept of parallel processing, Pipelining, Forms of parallel processing, interconnect network	5
	Total	28

BT5EC4-02: Electromagnetic Waves

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	Transmission Lines-Equations of Voltage and Current on TX line, Propagation constant and characteristic impedance, and reflection coefficient and VSWR, Impedance Transformation on Loss-less and Low loss Transmission line, Power transfer on TX line, Smith Chart, Admittance Smith Chart, Applications of transmission lines: Impedance Matching, use transmission line sections as circuit elements.	08
3	Maxwell's Equations-Basics of Vectors, Vector calculus, Basic laws of Electromagnetics, Maxwell's Equations, Boundary conditions at Media Interface.	03
4	Uniform Plane Wave-Uniform plane wave, Propagation of wave, Wave polarization, Poincare's Sphere, Wave propagation in conducting medium, phase and group velocity, Power flow and Poynting vector, Surface current and power loss in a conductor.	08
5	Plane Waves at a Media Interface-Plane wave in arbitrary direction, Reflection and refraction at dielectric interface, Total internal reflection, wave polarization at media interface, Reflection from a conducting boundary.	07
6	Waveguides- Wave propagation in parallel plate waveguide, Analysis of waveguide general approach, Rectangular waveguide, Modal propagation in rectangular waveguide, Surface currents on the waveguide walls, Field visualization, Attenuation in waveguide.	08
7	Radiation-Solution for potential function, Radiation from the Hertz dipole, Power radiated by hertz dipole, Radiation Parameters of antenna, receiving antenna, Monopole and Dipole antenna	07
	Total	42

BT5EC4-03: Control system

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Introduction to control problem- Industrial Control examples. Transfer function. System with dead-time. System response. Control hardware and their models: potentiometers, synchros, LVDT, dc and ac servomotors, tacho-generators, electro hydraulic valves, hydraulic servomotors, electro pneumatic valves, pneumatic actuators. Closed-loop systems. Block diagram and signal flow graph analysis.	8
3	Feedback control systems- Stability, steady-state accuracy, transient accuracy, disturbance rejection, insensitivity and robustness. proportional, integral and derivative systems. Feed forward and multi-loop control configurations, stability concept, relative stability, Routhstability criterion.	7
4	Time response of second-order systems- steady-state errors and error constants. Performance specifications in time-domain. Root locus method of design. Lead and lag compensation.	6
5	Frequency-response analysis- Polar plots, Bode plot, stability in frequency domain, Nyquistplots. Nyquist stability criterion. Performance specifications in frequency-domain. Frequency domain methods of design, Compensation & their realization in time & frequency domain. Lead and Lag compensation. Op-amp based and digital implementation of compensators. Tuning of process controllers. State variable formulation and solution.	8
6	State variable Analysis- Concepts of state, state variable, state model, state models for linear continuous time functions, diagonalization of transfer function, solution of state equations, concept of controllability & observability.	6

7	Introduction to Optimal control & Nonlinear control, Optimal Control problem, Regulator problem, Output regulator, treking problem. Nonlinear system – Basic concept & analysis.	6
	Total	42

BT5EC4-04: Digital Signal Processing

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Discrete time signals: Sequences; representation of signals on orthogonal basis; Sampling and reconstruction of signals; Discrete systems attributes, Z-Transform, Analysis of LSI systems, frequency Analysis, Inverse Systems	10
3	Discrete Fourier Transform (DFT), Fast Fourier Transform Algorithm, Implementation of Discrete Time Systems	9
4	Design of FIR Digital filters: Window method, Park-McClellan's method. Design of IIR DigitalFilters: Butterworth, Chebyshev and Elliptic Approximations; Lowpass, Bandpass, Bandstop and High pass filters.	10
5	Effect of finite register length in FIR filter design. Parametric and non- parametric spectral estimation. Introduction to mult-irate signal processing. Application of DSP.	10
	Total	40

BT5EC4-05: Microwave Theory & Techniques

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Introduction to Microwaves-History of Microwaves, Microwave Frequency bands; Applications of Microwaves: Civil and Military, Medical, EMI/ EMC.	4
3	Mathematical Model of Microwave Transmission-Concept of Mode, Features of TEM, TE and TM Modes, Losses associated with microwave transmission, Concept of Impedance in Microwave transmission.	5
4	Analysis of RF and MicrowaveTransmission Lines-Coaxialline, Rectangular waveguide, Circular waveguide, Strip line, Micro strip line.	4
5	Microwave Network Analysis-Equivalent voltages and currents for non- TEMlines, Network parameters for microwave circuits, Scattering Parameters.	4
6	Passive and Active Microwave Devices-Microwave passive components: Directional Coupler, Power Divider, Magic Tee, Attenuator, Resonator. Microwave active components: Diodes, Transistors, Oscillators, Mixers. Microwave Semiconductor Devices: Gunn Diodes, IMPATT diodes, Schottky Barrier diodes, PIN diodes. Microwave Tubes: Klystron, TWT, Magnetron.	6
7	Microwave Design Principles-Impedance transformation, Impedance Matching, Microwave Filter Design, RF and Microwave Amplifier Design, Microwave Power Amplifier Design, Low Noise Amplifier Design, Microwave Mixer Design, Microwave Oscillator Design. Microwave Antennas- Antenna parameters, Antenna for ground based systems, Antennas for airborne and satellite borne systems, Planar Antennas.	6

8	Microwave Measurements-Power, Frequency and impedance measurement at microwave frequency, Network Analyzer and measurement of scattering parameters, Spectrum Analyzer and measurement of spectrum of a microwave signal, Noise at microwave frequency and measurement of noise figure. Measurement of Microwave antenna parameters.	6
9	Microwave Systems-Radar, Terrestrial and Satellite Communication, Radio Aidsto Navigation, RFID, GPS. Modern Trends in Microwaves Engineering- Effect of Microwaves on human body, Medical and Civil applications of microwaves, Electromagnetic interference and Electromagnetic Compatibility (EMI & EMC), Monolithic Microwave ICs, RFMEMS for microwave components, Microwave Imaging.	6
	Total	42

BT5EC5-11: Bio-Medical Electronics

Credit: 2 **Max. Marks: 100(IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Brief introduction to human physiology. Biomedical transducers: displacement, velocity, force, acceleration, flow, temperature, potential, dissolved ions and gases.	9
3	Bio-electrodes and biopotential amplifiers for ECG, EMG, EEG, etc.	7
4	Measurement of blood temperature, pressure and flow. Impedance plethysmography. Ultrasonic, X-ray and nuclear imaging. Prostheses and aids: pacemakers, defibrillators, heart-lung machine, artificial kidney, aids for the handicapped. Safety aspects.	11
	Total	28

BT5EC5-12: Embedded Systems

Credit: 2 **Max. Marks: 100(IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	The concept of embedded systems design, Embedded microcontroller cores, embedded memories.	5
3	Examples of embedded systems, Technological aspects of embedded systems: interfacing between analog and digital blocks, signal conditioning, digital signal processing. Sub system interfacing, interfacing with external systems, user interfacing.	10
4	Design tradeoffs due to process compatibility, thermal considerations, etc., Software aspects of embedded systems: real time programming languages and operating systems for embedded systems.	12
	Total	28

BT5EC5-13: Probability Theory & Stochastic Process

Credit: 2 **Max. Marks: 100(IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Sets and set operations; Probability space; Conditional probability and Bayes theorem; Combinatorial probability and sampling models.	5
3	Discrete random variables, probability mass function, probability distribution function, example random variables and distributions; Continuous random variables, probability density function, probability distribution function, example distributions;	6
4	Joint distributions, functions of one and two random variables, moments of random variables; Conditional distribution, densities and moments; Characteristic functions of a random variable; Markov, Chebyshev and Chernoff bounds;	6
5	Random sequences and modes of convergence (everywhere, almost everywhere, probability, distribution and mean square); Limit theorems; Strong and weak laws of large numbers, central limit theorem	5
6	Random process. Stationary processes. Mean and covariance functions. Ergodicity. Transmission of random process through LTI. Power spectral density.	4
	Total	27

BT5EC5-14: Satellite Communication

Credit: 2 **Max. Marks: 100(IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Introduction to Satellite Communication: Principles and architecture of satellite Communication, Brief history of Satellite systems, advantages, disadvantages, applications and frequency bands used for satellite communication.	4
3	Orbital Mechanics: Orbital equations, Kepler's laws, Apogee and Perigee for an elliptical orbit, evaluation of velocity, orbital period, angular velocity etc. of a satellite, concepts of Solar day and Sidereal day.	4
4	Satellite sub-systems: Study of Architecture and Roles of various sub- systems of a satellite system such as Telemetry, tracking, command and monitoring (TTC & M), Attitude and orbit control system (AOCS), Communication sub-system, power sub-systems etc.	5
5	Typical Phenomena in Satellite Communication: Solar Eclipse on satellite, its effects, remedies for Eclipse, Sun Transit Outage phenomena, its effects and remedies, Doppler frequency shift phenomena and expression for Doppler shift. Satellite link budget	5
6	Flux density and received signal power equations, Calculation of System noise temperature for satellite receiver, noise power calculation, Drafting of satellite link budget and C/N ratio calculations in clear air and rainy conditions.	4
7	Modulation and Multiple Access Schemes: Various modulation schemes used in satellite communication, Meaning of Multiple Access, Multiple access schemes based on time, frequency, and code sharing namely TDMA, FDMA and CDMA.	4
	Total	27

BT5EC4-21: RF Simulation Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P **End Term Exam: 2 Hours**

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
2	Study of field pattern of various modes inside a rectangular and circular waveguide.
3	Find the change in characteristics impedance and reflection coefficients of the transmission line by changing the dielectric properties of materials embedded between two conductors.
4	Design and simulate the following Planar Transmission Lines: I. Strip and micro-strip lines II. Parallel coupled strip line III. Coplanar and Slot lines Determine their field patterns and characteristic impedance.
5	Design and simulate the following: I. 3-dB branch line coupler II. Wilkinson power divider III. Hybrid ring IV. Backward wave coupler V. Low pass filters VI. Band pass filters
6	Design RF amplifier using microwave BJT.
7	Design RF amplifier using microwave FET.

BT5EC4-22: Digital Signal Processing Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P **End Term Exam: 2 Hours**

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
2	Generation of continuous and discrete elementary signals (impulse, unit-step, ramp) using mathematical expression.
3	Perform basic operations on signals like adding, subtracting, shifting and scaling.
4	Perform continuous and discrete time Convolution (using basic definition).
5	Checking Linearity and Time variance property of a system using convolution, shifting.
6	To generate and verify random sequences with arbitrary distributions, means and variances for following: (a) Rayleigh distribution (b) Normal distributions: N(0,1). (c) Gaussian distributions: N (m, x) (d) Random binary wave.
7	To find DFT / IDFT of given DT signal.
8	N-point FFT algorithm.
9	To implement Circular convolution.
10	MATLAB code for implementing z-transform and inverse z-transform.
11	Perform inverse z-transform using residuez MATLAB function.
12	MATLAB program to find frequency response of analog LP/HP filters.
13	To design FIR filter (LP/HP) using windowing (rectangular, triangular, Kaiser) technique using simulink.

BT5EC4-23: Microwave Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**

0L+0T+2P **End Term Exam: 2 Hours**

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
2	Study of various microwave components and instruments like frequency meter, attenuator, detector and VSWR meter. (a) Measurement of guide wavelength and frequency using a X-band slotted line setup. (b) Measurement of low and high VSWR using a X-band slotted line setup.
3	Introduction to Smith chart, measurement of SWR, shift in minimum standing wave with unknown load and calculation of unknown load impedance using Smith chart.
4	Study the behavior of terminated coaxial transmission lines in time and frequency domain.
5	(a) Draw the V-I characteristics of a Gunn diode and determine the output power and frequency as a function of voltage. (b) Study the square wave modulation of microwave signal using PIN diode.
6	Study the square wave modulation of microwave signal using PIN diode.Study and measure the power division and isolation characteristics of a microstrip 3dB power divider.
7	Study of rat race hybrid ring (equivalent of waveguide Magic-Tee) in micro-strip.
8	(a) To study the characteristics of micro-strip 3dB branch line coupler, strip line backward wave coupler as a function of frequency and compare their bandwidth. (b) (b)Measure the microwave input, direct, coupled and isolated powers of a backward wave strip line coupler at the centre frequency using a power meter. From the measurements calculate the coupling, isolation and directivity of the coupler.

Mohan Lal Sukhadia University
Udaipur

B. Tech. Program
(Effective from session 2021-2022)

Electronics & Communication Engineering

Semesters VI

Syllabus

BT6EC3-01: Power Electronics

Credit: 2 **Max. Marks: 100(IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	SEMICONDUCTOR POWER DEVICES: Introduction. Basic characteristics &working of Power Diodes, Diac, Triac, MOSFETs, IGBT, GTO, Power Transistor and SCR- Principle of operation, V-I Characteristics, Turn-On mechanism and its applications	6
3	CONVERTERS: Basic concept, Working Principles of Single phase half Wave bridge converter, Single Phase Full Bridge Converter, 3 Phase Bridge Converter	5
4	INVERTERS: Voltage Source Inverter, Current Source Inverter, PWM Control of Voltage Source Converter and applications.	5
5	INDUSTRIAL POWER SUPPLIES: Principle of operation of choppers. Step up, Step down and reversible choppers. Chopper control techniques, High frequency electronic ballast, Switch Mode Power Supply: Fly back converter, forward/buck converter, Boost converter and buck-boost converter. Uninterruptible Power Supply.	6
6	MOTOR CONTROL: Introduction to speed control of DC motors using phase controlled converters and choppers, Basic idea of speed control of three phase induction motors using voltage and frequency control methods.	5
	Total	28

BT6EC4-02: Computer Network

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Queuing Theory- Pure birth, Pure death & Birth-death processes, Mathematical models for M/M/1, M/M/ □ , M/M/m, M/M/1/K and M/M/m/m queues. Little's formula.	7
3	Introduction to computer networks and the Internet: Application layer: Principles of network applications, The Web and Hyper Text Transfer Protocol, File transfer, Electronic ail, Domain name system, Peer-to-Peer file sharing, Socket programming, Layering concepts. Packet switching, Blocking in packet switches, Three generations of packet switches, switch fabric, Buffering, Multicasting, Statistical Multiplexing.	9
4	Transport layer: Connectionless transport - User Datagram Protocol, Connection oriented transport – Transmission Control Protocol, Remote Procedure Call. Congestion Control and Resource Allocation: Issues in Resource Allocation, Queuing Disciplines, TCP congestion Control, Congestion Avoidance Mechanisms and Quality of Service.	9
5	Network layer: Virtual circuit and Datagram networks, Router, Internet Protocol, Routing algorithms, Broadcast and Multicast routing	7
6	Link layer: ALOHA, Multiple access protocols, IEEE 802 standards, Local Area Networks, addressing, Ethernet, Hubs, Switches. Fundamental of SDN, Open flow.	7
	Total	40

BT6EC4-03: Fiber Optics Communications

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Introduction to vector nature of light, propagation of light, propagation of light in a cylindrical dielectric rod, Ray model, wave model. Different types of optical fibers, Modal analysis of a step index fiber.	8
3	Signal degradation on optical fiber due to dispersion and attenuation. Fabrication of fibers and measurement techniques like OTDR	7
4	Optical sources - LEDs and Lasers, Photo-detectors - pin-diodes, APDs, detector responsivity, noise, optical receivers. Optical link design - BER calculation, quantum limit, power penalties.	8
5	Optical switches - coupled mode analysis of directional couplers, electro- optic switches. Optical amplifiers - EDFA, Raman amplifier.	8
6	WDM and DWDM systems. Principles of WDM networks.Nonlinear effects in fiber optic links. Concept of self-phase modulation, groupvelocity dispersion and solition based communication.	8
	Total	40

BT6EC4-04: Antennas and Propagation

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Fundamental Concepts-Physical concept of radiation, Radiation pattern, near andfar-field regions, reciprocity, directivity and gain, effective aperture, polarization, input impedance, efficiency, Friis transmission equation, radiation integrals and auxiliary potential functions.	7
3	Radiation from Wires and Loops-Infinitesimal dipole, finite-length dipole, linear elements near conductors, dipoles for mobile communication, small circular loop.	6
4	Aperture and Reflector Antennas-Huygens' principle, radiation from rectangular and circular apertures, design considerations, Babinet's principle, Radiation from sectoral and pyramidal horns, design concepts, prime-focus parabolic reflector and cassegrain antennas.	7
5	Broadband Antennas-Log-periodic and Yagi-Uda antennas, frequency independent antennas, broadcast antennas.	5
6	Micro strip Antennas-Basic characteristics of micro strip antennas, feeding methods, methods of analysis, design of rectangular and circular patch antennas.	6
7	Antenna Arrays-Analysis of uniformly spaced arrays with uniform and non-uniform excitation amplitudes, extension to planar arrays, synthesis of antenna arrays using Schelkun off polynomial method, Woodward-Lawson method.	5
8	Basic Concepts of Smart Antennas-Concept and benefits of smart antennas, fixed weight beam forming basics, Adaptive beam forming.	4
9	Different modes of Radio Wave propagation used in current practice.	1
	Total	42

BT6EC4-05: Information Theory and Coding

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	Basics of information theory, entropy for discrete ensembles; Shannon's noiseless coding theorem; Encoding of discrete sources.	15
3	Markov sources; Shannon's noisy coding theorem and converse for discrete channels; Calculation of channel capacity and bounds for discrete channels; Application to continuous channels.	15
4	Techniques of coding and decoding; Huffman codes and uniquely detectable codes; Cyclic codes, convolutional arithmetic codes.	10
	Total	41

BT6EC5-11: Introduction to MEMS

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Introduction and Historical Background.	1
3	Mechanics of solids in MEMS/NEMS: Stresses, Strain, Hookes's law, Poisson effect, Linear Thermal Expansion, Bending; Energy methods, Overview of Finite Element Method, Modeling of Coupled Electromechanical Systems.	14
4	Scaling Effects. Micro/Nano Sensors, Actuators and Systems overview: Case studies. Review of Basic MEMS fabrication modules: Oxidation, Deposition Techniques, Lithography (LIGA), and Etching.	14
5	Micromachining: Surface Micromachining, sacrificial layer processes, Stiction; Bulk Micromachining, Isotropic Etching and Anisotropic Etching, Wafer Bonding.	10
	Total	40

BT6EC5-12: Nano Electronics

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	Introduction to nanotechnology, meso structures, Basics of Quantum Mechanics: Schrodinger equation, Density of States. Particle in a box Concepts, Degeneracy. Band Theory of Solids. Kronig-Penny Model. Brillouin Zones.	15
3	Shrink-down approaches: Introduction, CMOS Scaling, The nano scale MOSFET, Finfets, Vertical MOSFETs, limits to scaling, system integration limits (interconnect issues etc.).	10
4	Resonant Tunneling Diode, Coulomb dots, Quantum blockade, Single electron transistors, Carbon nanotube electronics, Band structure and transport, devices, applications, 2D semiconductors and electronic devices, Graphene, atomistic simulation	14
	Total	40

BT6EC5-13: Neural Network And Fuzzy Logic Control

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	NEUROPHYSIOLOGY: Introduction: Elementary neurophysiology – From neurons to ANNs - Neuron model McCulloch-Pitts model, Hebbian Hypothesis; limitations of single-layered neural networks. Applications Of Neural Networks: Pattern classification, Associative memories, Optimization, Applications in Image Processing-Iris, finger print & face, Applications in decision making.	8
3	THE PERCEPTRON: The Perceptron and its learning law. Classification of linearly separable patterns. Linear Networks: Adaline - the adaptive linear element. Linear regression. The Wiener-Hopf equation. The Least- Mean-Square (Widrow-Hoff) learning algorithm. Method of steepest descent. Adaline as a linear adaptive filter. A sequential regression algorithm. Multi-Layer Feed forward Neural Networks: Multi-Layer Perceptrons. Supervised Learning. Approximation and interpolation of functions. Back-Propagation Learning law. Fast training algorithms. Applications of multilayer perceptrons: Image coding, Paint-quality inspection, Nettalk.	9
4	FUZZY LOGIC: Introduction -Uncertainty & precision, Statistics and random process, Uncertainty in information, Fuzzy sets and membership. Membership Functions: Features of membership function. Standard forms and boundaries, Fuzzification, Membership value assignment – Intuition, Inference, Neural networks. Fuzzy To Crisp Conversions: Maximum membership principle.	7
5	DEFUZZIFICATION METHODS- Centroid method, Weighted average method, Meanmax membership. Fuzzy Rule Based Systems: Natural language, linguistic hedges, Rule based system –Canonical rule forms, Decomposition of compound rules, Likelihood and truth qualification Aggregation of Fuzzy rules. Graphical techniques of reference.	8

6	FUZZY CONTROL SYSTEM- Simple Fuzzy Logic controller, General FLC, Control System Design Problem Control (Decision) Surface, Assumptions in a Fuzzy Control System Design, Special forms of FLC system models, Industrial application: Aircraft Landing Control Problem. Fuzzy Engineering Process Control: Classical Feedback Control, Classical PID Control, Multi-input, Multi-output (MIMO) Control Systems, Fuzzy Statistical Process Control	9
	Total	42

BT6EC5-14: High Speed Electronics

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	1
2	Transmission line theory (basics) crosstalk and non ideal effects; signal integrity: impact of packages, vias, traces, connectors; non-ideal return current paths, high frequency power delivery, methodologies for design of high speed buses; radiated emissions and minimizing system noise; Noise Analysis: Sources, Noise Figure, Gain compression, Harmonic distortion, Inter-modulation, Cross-modulation, Dynamic range	10
3	Devices: Passive and active, Lumped passive devices (models), Active (models, low vs High frequency)	6
4	RF Amplifier Design, Stability, Low Noise Amplifiers, Broadband Amplifiers (and Distributed)Power Amplifiers, Class A, B, AB and C, D E Integrated circuit realizations, Cross-over distortion Efficiency RF power output stages	8
5	Mixers –Up conversion Down conversion, Conversion gain and spurious response. OscillatorsPrinciples.PLL Transceiver architectures	8
6	Printed Circuit Board Anatomy, CAD tools for PCB design, Standard fabrication, Micro via Boards. Board Assembly: Surface Mount Technology, Through Hole Technology, Process Control and Design challenges.	8
	Total	41

BT6EC4-21: Computer Network Lab

Credit: 2 **Max. Marks: 100(IA:60, ETE:40)**

0L+0T+4P **End Term Exam: 2 Hours**

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
2	PRELIMINARIES: Study and use of common TCP/IP protocols and term viz. telnet rlogin ftp, ping, finger, Socket, Port etc.
3	DATA STRUCTURES USED IN NETWORK PROGRAMMING: Representation of unidirectional, Directional weighted and unweighted graphs.
4	ALGORITHMS IN NETWORKS: computation of shortest path for one source- one destination and one source –all destination
5	SIMULATION OF NETWORK PROTOCOLS: i. Simulation of M/M/1 and M/M/1/N queues. ii. Simulation of pure and slotted ALOHA. iii. Simulation of link state routing algorithm.
6	Case study : on LAN Training kit i. Observe the behavior& measure the throughput of reliable data transfer protocols under various Bit error rates for following DLL layer protocols- a. Stop & Wait b. Sliding Window : Go-Back-N and Selective Repeat ii. Observe the behavior& measure the throughput under various network load conditions for following MAC layer Protocols a. Aloha b. CSMA, CSMA/CD & CSMA/CA c. Token Bus & Token Ring
7	Software and hardware realization of the following: i. Encoding schemes: Manchester, NRZ. ii. Error control schemes: CRC, Hamming code.

BT6EC4-22: Antenna and Wave Propagation Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**

0L+0T+2P **End Term Exam: 2 Hours**

SN	Contents
	PART-I (Antenna)
1	Study the gain pattern, HPBW, FNBW and Directivity of a dipole antenna.
2	Measurement of Radiation Pattern, Gain, HPBW of a folded dipole antenna.
3	Measurement of Radiation Pattern, Gain, HPBW of a loop antenna
4	Measurement of Radiation Pattern, Gain, VSWR, input impedance and reflection coefficient for given Monopole antenna
5	Measurement of Radiation Pattern, Gain, VSWR, input impedance and reflection coefficient for given Yagi antennas
6	Study of the Radiation Pattern, Gain, HPBW of a horn antenna
7	Study of the Radiation Pattern, Gain, HPBW of a reflector antennas
8	Study the radiation pattern, gain, VSWR, and input impedance of a rectangular microstrip patch antenna
9	Study the effect of inset feed on the input impedance of a rectangular patch antenna
10	Study the effect of ground plane on the radiation pattern of an antenna
11	Study antenna designing in CST Microwave Studio
12	Design a rectangular microstrip patch antenna using CST MWS
	PART-II (Optical Fiber) To perform following experiments based on Fiber Optic Trainer.
13	To set up Fiber Optic Analog link and Digital link.
14	Measurement of Propagation loss and numerical aperture.

BT6EC4-23: Electronics Design Lab

Credit: 2 **Max. Marks: 100(IA:60, ETE:40)**
0L+0T+4P **End Term Exam: 2 Hours**

SN	Contents
	To design the following circuits, assemble these on bread board and test them and Simulation of these circuits with the help of appropriate software.
1	Op-Amp characteristics and get data for input bias current measure the output-offset voltage and reduce it to zero and calculate slew rate.
2	Op-Amp in inverting and non-inverting modes.
3	Op-Amp as scalar, summer and voltage follower.
4	Op-Amp as differentiator and integrator.
5	Design LPF and HPF using Op-Amp 741
6	Design Band Pass and Band reject Active filters using Op-Amp 741.
7	Design Oscillators using Op-Amp (i) RC phase shift (ii) Hartley (iii) Colpitts
8	Design (i) Astable (ii) Monostable multivibrators using IC-555 timer
9	Design Triangular & square wave generator using 555 timer.
10	Design Amplifier (for given gain) using Bipolar Junction Transistor.
11	Op-Amp characteristics and get data for input bias current measure the output-offset voltage and reduce it to zero and calculate slew rate.
12	Op-Amp in inverting and non-inverting modes.
13	Op-Amp as scalar, summer and voltage follower.

6EC4-24: Power Electronics Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**
0L+0T+2P **End Term Exam: 2 Hours**

SN	Contents
1	Study the characteristics of SCR and observe the terminal configuration, Measure the breakdown voltage, latching and holding current. Plot V-I characteristics.
2	Perform experiment on triggering circuits for SCR. i.e. R triggering, R-C triggering and UJT triggering circuit.

3	Study and test AC voltage regulators using triac, antiparallel thyristors and triac&diac.
4	Study and obtain the waveforms for single-phase bridge converter.
5	Perform experiment on single phase PWM inverter.
6	Perform experiment on buck, boost and buck-boost regulators.
7	Control speed of a dc motor using a chopper and plot armature voltage versus speed characteristic.
8	Control speed of a single-phase induction motor using single phase AC voltage regulator.
9	I. Study single-phase dual converter. II. Study speed control of dc motor using single-phase dual converter.
10	Study single-phase cyclo converter.
11	Perform experiment on Motor control – open loop & closed loop
12	Design, observe and perform experiment on various type of pulse generation from DSP/ FPGA Platform. Perform experiment for PWM inverters and choppers.

Mohan Lal Sukhadia University
Udaipur

B. Tech. Program
(Effective from session 2021-2022)

Electronics & Communication Engineering

Semesters VII

Syllabus

BT7EC5-11: VLSI Design (program elective-3)

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	INTRODUCTION TO MOSFET- Basic MOS transistors, Enhancement Mode transistor action, Depletion Mode transistor action, NMOS and CMOS fabrication. Aspects of threshold voltage, threshold voltage with body effect. I_{ds} versus V_{ds} relationship, channel length modulation. Transistor Trans-conductance g_m. MOS transistor circuit Model, Model parameter (oxide and junction capacitor, channel resistance) variation with scaling and biasing. High order effects (i.e. sub threshold conduction, hot electron effect, narrow channel effect and punch through effect.	12
3	CMOS LOGIC CIRCUITS- NMOS inverter (resistive and active load), Pull up to Pull-down ratio(β_p/β_n) for a NMOS Inverter and CMOS Inverter, determination of inverter parameter (VIL, VIH VOL VOH) and Noise Margin. Speed and power dissipation analysis of CMOS inverter. Combinational Logic, NAND Gate, NOR gate, XOR gate, Compound Gates, 2 input CMOS Multiplexer, Memory latches and registers, Transmission Gate (TG), estimation of Gate delays, Power dissipation and Transistor sizing. Basic physical design of simple Gates and Layout issues. Layout issues for CMOS inverter, Layout for NAND, NOR and Complex Logic gates, Layout of TG, Layout optimization using Eular path. DRC rules for layout and issues of interconnects, Latch up problem.	11
4	Dynamic CMOS circuits- Clocked CMOS (C^2MOS) logic, DOMINO logic, NORA logic, NP(ZIPPER) logic, PE (pre-charge and Evaluation) Logic. Basic Memory circuits, SRAM and DRAM.	08
5	Physical Design- Introduction to ECAD tools for front and back end design of VLSI circuits. Custom /ASIC design, Design using FPGA and VHDL. VHDL Code for simple Logic gates, flip-flops, shift registers.	08
	Total	40

BT7EC5-12: Mixed Signal Design(program elective-3)

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**
3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	Analog and discrete-time signal processing, introduction to sampling theory; Analog continuous time filters: passive and active filters; Basics of analog discrete-time filters and Z-transform.	10
3	Basic logic gates with BJT and MOSFET combination, Switched-capacitor filters- Non idealities in switched-capacitor filters; Switched-capacitor filter architectures; Switched-capacitor filter applications.	07
4	Basics of data converters; Successive approximation ADCs, Dual slope ADCs, Flash ADCs, Pipeline ADCs, Hybrid ADC structures, High-resolution ADCs, DACs.	08
5	Mixed-signal layout, Interconnects and data transmission; Voltage-mode signal aligned data transmission; Current-mode signaling and data transmission.	08
6	Introduction to frequency synthesizers and synchronization; Basics of PLL, Analog PLLs; Digital PLLs; DLLs	06
	Total	40

BT7EC5-13: CMOS Design (program elective-3)

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	**01**
2	Review of MOS transistor models, Non-ideal behavior of the MOS Transistor, Transistor as a switch, Inverter characteristics	**08**
3	Integrated Circuit Layout: Design Rules, Parasitic, Delay: RC Delay model, linear delay model, logical path efforts, Power, interconnect and Robustness in CMOS circuit layout	**07**
4	Combinational Circuit Design: CMOS logic families including static, dynamic and dual rail logic. NAND Gate, NOR gate, XOR gate, Compound Gates, 2 input CMOS Multiplexer, Memory latches and registers, Transmission Gate, estimation of Gate delays, Power dissipation and Transistor sizing. Basic physical design of simple Gates and Layout issues. Layout issues for CMOS inverter, Layout for NAND, NOR and Complex Logic gates,	**10**
5	Dynamic CMOS circuits- Clocked CMOS (C^2MOS) logic, DOMINO logic, NORA logic, NP(ZIPPER) logic, PE (pre-charge and Evaluation) Logic. Basic Memory circuits, SRAM and DRAM.	**08**
6	Physical Design- Introduction to ECAD tools for first and back end design of VLSI circuits. Custom /ASIC design, Design using FPGA and VHDL. VHDL Code for simple Logic gates, flip-flops, shift registers.	**06**
	Total	**40**

BT7EC4-21: VLSI Design Lab

Credit: **Max. Marks: 100(IA:60, ETE:40)**

0L+0T+4P

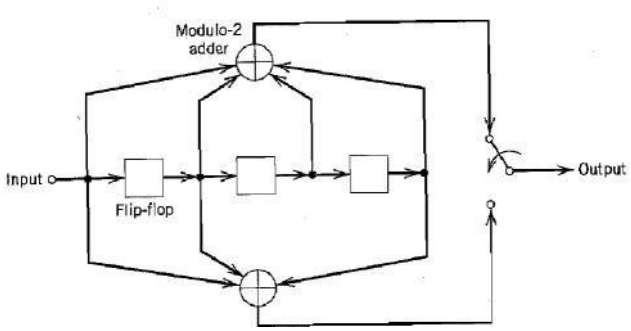
SN	Contents
1	Introduction: Objective, scope and outcome of the course.
PART-A	Step1 Write the VHDL/Verilog code using VHDL software for following experiment and simulate them. Step 2. Burn the Written code in Xilling Board and test the output with real input signal
1	Design and simulate all the logic gates with 2 inputs using VHDL/Verilog.
2	Design and simulate 2-to-4 decoder,3-to-8 encoder and 8X1 multiplexer using VHDL/Verilog.
3	Design and simulate half adder and full adder using VHDL (data flow method)/Verilog.
4	Design and simulate D, T and J-K flip flop using VHDL/Verilog.
5	Design a 4bit binary Asynchronous and synchronous counter. Obtain its number of gates, area, and speed and power dissipation.
6	Design a 4- bit Serial in-serial out shift register. Obtain its number of gates, area, and speed and power dissipation.
PART-B	Step-1 Design and simulate following experiment using ECAD software Viz. Mentor graphics, Orcade Pspice, Cadence etc. Step-2 Draw the layout (without any DRC error)of the schematic obtain in step 1 and obtain post layout simulation using appropriate ECAD software.
1	Design and simulate all the logic gates (NOT, NAND and NOR) with 2 inputs in CMOS Technology.
2	Design and simulate Y = AB (C+D), Y = A+B(C+D) and 4X1 multiplexer using CMOS Technology.
3	Design and simulate half adder and full adder using CMOS Technology.
4	Design and simulate SR flip flop using CMOS Technology.
5	Design and Simulate any DRAM cell.

BT7EC4-22: Advance Communication Lab (MATLAB Simulation)

Credit: 1 **Max. Marks: 50 (IA:30, ETE:20)**

0L+0T+2P

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
Part-A	**Analog-to-digital conversion** 1. Generate a sinusoidal signal. Sample and reconstruct a signal through interpolation. Vary the sampling rate below and above the Nyquist rate and hence verify the Sampling theorem. 2. Generate a sequence of length 500 of zero-mean, unit variance Gaussian random variables. Using a uniform PCM scheme, quantize this sequence to 16, 64 and 128 levels. (a). Find and compare the resulting signal-to-quantization noise ratios. (b). Find the first ten values of the sequence, the corresponding quantized values and the corresponding code words for each case. (c). Plot the quantization error and the quantized value as a function of the input value for each case. **Digital modulation techniques** 3. Simulate the transmitter and receiver for QPSK. Plot the signal and signal constellation diagram. Plot the average probability of symbol error as a function of SNR E_b/N_o, where E_b is the transmitted energy per bit and $N_o/2$ is the double sided power spectral density of additive white Gaussian noise (AWGN) with zero mean. 4. Simulate the transmitter and receiver for 16-QAM. Plot the signal and signal constellation diagram. Plot the average probability of symbol error as a function of SNR E_b/N_o, where E_b is the transmitted energy per bit and $N_o/2$ is the double sided power spectral density of additive white Gaussian noise (AWGN) with zero mean.
PART-B Attempt any four experiment	1. Find all the code words of the (15,11) Hamming code and verify that its minimum distance is equal to 3. 2. Generate an equiprobable random binary information sequence of length 15. Determine the output of the convolutional encoder shown below for this sequence.



3. Generate the L=31 Gold sequences. Consider a time-synchronous CDMA system (direct sequence spread spectrum) having four users, each employing a distinct Gold sequence of length L=31 and the binary (±1) modulation of their representative Gold sequences. The receiver for each user correlates the composite CDMA received signal, which is corrupted by AWGN (added on a chip-by-chip basis) with each user's respective sequence. Using 10000 information bits, estimate and plot the probability of error for each user as a function of SNR.
4. Consider a MIMO (multiple-input, multiple-output) system with N_T = 2 transmit antennas and NR = 2 receive antennas. Generate the elements of the channel matrix **H** for a Rayleigh fading (frequency nonselective) AWGN channel and the corresponding inputs to the detectors for the two receive antennas.
5. Perform feature extraction from a given Image and use Principal Components as image descriptors.
6. By using an image dataset, train a Neural Network to recognize a given Image. Apply this in context to face/object recognition and calculate recognition accuracy of the training set.
7. Develop a Fuzzy Inference System (FIS) by using a set of fuzzy rule base between some key image parameters and calculate output after defuzzification.
8. Design a Fuzzy PID controller using Matlab for a Dc Motor.
9. Classify ECG signals using Neural networks.

BT7EC4-23: Optical Communication Lab

Credit: 1 **Max. Marks: 50 (IA:30, ETE:20)**

0L+0T+2P

SN	Contents
1	Introduction: Objective, scope and outcome of the course.
	Hardware based experiment;
1	To set up Fiber Optic Analog and fiber Optic Digital link.
2	Measurement of Propagation loss and numerical aperture.
3	Measurement of optical power bending loss in a plastic optical fiber.
4	Study and measure characteristics of fiber optic LED's, LDR and Laser diode.
5	OTDR Measurement of Fiber Length, Attenuation and Dispersion Loss.
	Software based experiment;
6	Design and simulate of single and multimode transmission in optical fiber system.
7	Show and simulate the optical system performance analysis using Eye diagram and measure the value of Q-factor & BER of optical signals.
8	Study and simulate the linear and parabolic waveguide structure use in optical fiber communication.
9	Design and simulate the Dispersion compensators for fiber optic communication.
10	Design and calculate the power budget for optical communication link.
11	Design and simulate the DWDM and WDM techniques use in optical communication.
12	Design and simulate the Fiber Bragg grating and find its transmission characteristics and optical band-gap.

Mohan Lal Sukhadia University
Udaipur

B. Tech. Program
(Effective from session 2021-2022)

Electronics & Communication Engineering

Semesters VIII

Syllabus

BT8EC5-11: ARTIFICIAL INTELLIGENCE AND EXPERT SYSTEMS

(program elective-4)

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	01
2	**Introduction to Artificial Intelligence:** Intelligent Agents, State Space Search, Uninformed Search, Informed Search, Two Players Games, Constraint Satisfaction Problems.	08
3	**Knowledge Representation:**Knowledge Representation And Logic, Interface in Propositional Logic, First Order Logic, Reasoning Using First Order Logic, Resolution in FOPL.	07
4	**KNOWLEDGE ORGANIZATION:** Rule based System, Semantic Net, Reasoning in Semantic Net Frames, Planning	08
5	**KNOWLEDGE SYSTEMS:** Rule Based Expert System, Reasoning with Uncertainty, Fuzzy Reasoning.	08
6	**KNOWLEDGE ACQUISITION:** Introduction to Learning, Rule Induction and Decision Trees, Learning Using neural Networks, Probabilistic Learning Natural Language Processing.	08
	Total	40

BT8EC5-12: Digital Image and Video Processing (program elective-4)

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	**01**
2	Digital Image Fundamentals-Elements of visual perception, image sensing and acquisition, image sampling and quantization, basic relationships between pixels neighborhood, adjacency, connectivity, distance measures.	**04**
3	Image Enhancements and Filtering-Gray level transformations, histogram equalization and specifications, pixel-domain smoothing filters – linear and order-statistics, pixel-domain sharpening filters – first and second derivative, two-dimensional DFT and its inverse, frequency domain filters – low-pass and high-pass.	**03**
4	Color Image Processing-Color models–RGB, YUV, HSI; Color transformations-formulation, color complements, color slicing, tone and color corrections; Color image smoothing and sharpening; Color Segmentation.	**04**
5	Image Segmentation- Detection of discontinuities, edge linking and boundary detection, Thresholding – global and adaptive, region-based segmentation.	**04**
6	Wavelets and Multi-resolution image processing- Uncertainty principles of Fourier Transform, Time-frequency localization, continuous wavelet transforms, wavelet bases and multi-resolution analysis, wavelets and Sub-band filter banks, wavelet packets.	**06**
7	Image Compression-Redundancy–inter-pixel and psycho-visual; Lossless compression – predictive, entropy; Lossy compression- predictive and transform coding; Discrete Cosine Transform; Still image compression standards – JPEG and JPEG-2000.	**06**
8	Fundamentals of Video Coding- Inter-frame redundancy, motion estimation techniques – full search, fast search strategies, forward and backward motion prediction, frame classification – I, P and B; Video sequence hierarchy – Group of pictures, frames, slices, macro-blocks and blocks; Elements of a video encoder and decoder; Video coding standards – MPEG and H.26X.	**06**
9	Video Segmentation- Temporal segmentation–shot boundary detection, hard-cuts and soft-cuts; spatial segmentation – motion-based; Video object detection and tracking.	**06**
	Total	**40**

BT8EC5-13: Adaptive Signal Processing (program elective-4)

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction: Objective, scope and outcome of the course.	**01**
2	General concept of adaptive filtering and estimation, applications and motivation, Review of probability, random variables and stationary random processes, Correlation structures, properties of correlation matrices.	**08**
3	Optimal FIR (Wiener) filter, Method of steepest descent, extension to complex valued The LMS algorithm (real, complex), convergence analysis, weight error correlation matrix, excess mean square error and mis-adjustment Variants of the LMS algorithm: the sign LMS family, normalized LMS algorithm, block LMS and FFT based realization, frequency domain adaptive filters, Sub-band adaptive filtering.	**07**
4	Signal space concepts - introduction to finite dimensional vector space theory, subspace, basis, dimension, linear operators, rank and nullity, inner product space, orthogonality, Gram-Schmidt orthogonalization, concepts of orthogonal projection, orthogonal decomposition of vector spaces.	**08**
5	Vector space of random variables, correlation as inner product, forward and backward projections, Stochastic lattice filters, recursive updating of forward and backward prediction errors, relationship with AR modeling, joint process estimator, gradient adaptive lattice.	**08**
6	Introduction to recursive least squares (RLS), vector space formulation of RLS estimation, pseudo-inverse of a matrix, time updating of inner products, development of RLS lattice filters, RLS transversal adaptive filters. Advanced topics: affine projection and subspace based adaptive filters, partial update algorithms, QR decomposition and systolic array.	**08**
	Total	**40**

BT8EC4-21: IOT Lab

Credit: 1 **Max. Marks: 50**

(IA:30, ETE:20) 0L+0T+2P

LIST OF PRACTICALS	
1.	Study the fundamental of IOT softwares and components.
2.	Familiarization with Arduino /Raspberry Pi and perform necessary software installation.
3.	To interface LED/Buzzer with Arduino/Raspberry Pi and write a program to turn ON LED for 1 sec after every 2 seconds.
4.	To interface Push button/Digital sensor (IR/LDR) with Arduino /Raspberry Pi and write a program to turn ON LED when push button is pressed or at sensor detection.
5.	To interface DHT11 sensor with Arduino/Raspberry Pi and write a program to print temperature and humidity readings.
6.	To interface motor using relay with Arduino/Raspberry Pi and write a program to turn ON motor when push button is pressed.
7.	To interface OLED with Arduino/Raspberry Pi and write a program to print temperature and humidity readings on it.
8.	To interface Bluetooth with Arduino/Raspberry Pi and write a program to send sensor data to smart phone using Bluetooth.
9.	To interface Bluetooth with Arduino/Raspberry Pi and write a program to turn LED ON/OFF when '1'/'0' is received from smart phone using Bluetooth.
10	Write a program on Arduino/Raspberry Pi to upload temperature and humidity data to thing speak cloud.
11.	Write a program on Arduino/Raspberry Pi to retrieve temperature and humidity data from thing speak cloud.
12.	To install MySQL database on Raspberry Pi and perform basic SQL queries.
13.	Write a program to create UDP server on Arduino/Raspberry Pi and respond with humidity data to UDP client when requested.
14.	Write a program to create TCP server on Arduino/Raspberry Pi and respond with humidity data to TCP client when requested.

BT8EC4- 22 Skill Development Lab

Credit: 1 **Max. Marks : 50 (IA:30,ETE:20)**

0L+0T+2P

Part A: Training	
SN	**Contents**
1	Introduction: Objective, scope and outcome of the lab.
	Every student has to learn any two software from the following list, with consultation of their lab in charge. Students may get online certification or is advised to learn these from available freeware. Students may register online training courses from institutes of repute i.e. IITs/NITs/AICTE/MHRD, etc. Industrial experts /professional may be deputed to train the students in department.
1	Network simulator (NS_2)
2	Lab view
3	Software for Robotics/Artificial intelligence (AI) /machine learning
4	Java
5	Python

PART B: Implementation	
SN	**Contents**
1	**Student has to complete any one assignment with detailed project report based on the software/tool learn in part A.**
2	Student cab select any Social engineering project: Any problem of the society can be taken which can be solved with the help of electronics engineering software and gadgets.
3	Student can select Startup for innovation/entrepreneurship.
4	Engineering solution of any Industrial problem. Sufficient number of such problem may be identified by the department from nearby industry and may be given to the student for innovative solutions under guidance of faculty.
	This lab may be evaluated by an external examiner from industry along with internal faculty.